



## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18  
Stylesheet Version v18.0

Title of Invention	Store-to-Load Forwarding Buffer Using Indexed Lookup						
Application Number:	10/615101						
Confirmation Number:	8643						
First Named Applicant:	Michael Filippo						
Attorney Docket Number:	550089400						
Art Unit:	2121						
Search string:	( 4521851 or 4594660 or 5404470 or 5465336 or 5467476 or 5651125 or 5710902 or 5717883 or 5748978 or 5799165 or 5835747 or 5884060 or 5884061 or 5923862 or 5996068 or 6016540 or 5276828 or 5440752 or 5487156 or 5490259 or 5557763 or 5625835 or 5652859 or 5742791 or 5761712 or 5768555 or 5802588 or 5832297 or 5887152 or 6122727 or 5619662 or 5781752 or 6212622 or 6212623 or 5850533 or 6266744 or 6393536 or 6108770 or 6502185 or 6542984 or 6651161 ).pn.						
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
DC	1	4521851	1985-06-04	Trubisky et al.			
DC	2	4594660	1986-06-10	Guenthner et al.			
DC	3	5404470	1995-04-04	Miyake			
DC	4	5465336	1995-11-07	Imai et al			
DC	5	5467476	1995-11-14	Kahle et al			
DC	6	5651125	1997-07-22	Witt et al			
DC	7	5710902	1998-01-20	Sheaffer et al			
DC	8	5717883	1998-02-10	Sager			
DC	9	5748978	1998-05-05	Narayan et al			
DC	10	5799165	1998-08-25	Favor et al			
DC	11	5835747	1998-11-10	Trull			

DC	12	5884060	1999-03-16	Vegesna et al.
DC	13	5884061	1999-03-16	Hesson et al
DC	14	5923862	1999-07-13	Nguyen et al
DC	15	5996068	1999-11-30	Dwyer III et al
DC	16	6016540	2000-01-18	Zaidi et al
DC	17	5276828	1994-01-04	Dion
DC	18	5440752	1995-08-08	Lentz et al
DC	19	5487156	1996-01-23	Popescu et al
DC	20	5490259	1996-02-06	Hiraoka et al
DC	21	5557763	1996-09-17	Senter et al
DC	22	5625835	1997-04-29	Ebciooglu et al
DC	23	5652859	1997-07-29	Mulla et al
DC	24	5742791	1998-04-21	Mahalingaiah et al
DC	25	5761712	1998-06-02	Tran et al
DC	26	5768555	1998-06-16	Tran et al
DC	27	5802588	1998-09-01	Ramagopal et al
DC	28	5832297	1998-11-03	Ramagopal et al
DC	29	5887152	1999-03-23	Tran
DC	30	6122727	2000-09-19	Witt
DC	31	5619662	1997-04-08	Steely Jr et al
DC	32	5781752	1998-07-14	Moshovos et al
DC	33	6212622	2001-04-03	Witt
DC	34	6212623	2001-04-03	Witt
DC	35	5850533	1998-12-15	Panwar et al
DC	36	6266744	2001-07-24	Hughes et al
DC	37	6393536	2002-05-21	Hughes et al
DC	38	6108770	2000-08-22	Chrysos et al
DC	39	6502185	2002-12-31	Keller et al
DC	40	6542984	2003-04-01	Keller et al
DC	41	6651161	2003-11-18	Keller et al

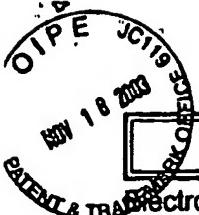
## Remarks

Note: Remarks are not for responding to an office action.

Other art will be submitted on a Form PTO 1449.

Signature

Examiner Name	Date
	21-DEC-2005



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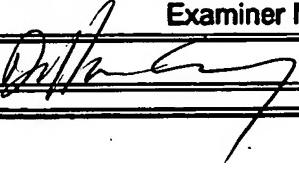
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Art Unit:	2121						
Search string:	( 6622237 or 6622235 or 5555432 or 5655096 or 5844059 or 5941983 or 5987594 or 6308259 or 6108769 or 6065105 or 5978900 or 6076153 or 6321328 or 5778219 or 5794029 or 5915117 or 6336178 or 5751983 or 5781790 or 5751985 or 6098166 or 5724536 or 6070238 or 5802575 ).pn.						
<b>RECEIVED</b> MAR 19 2004 Technology Center 2100							
<b>US Patent Documents</b>							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
DC	1	6622237	2003-09-16	Keller et al			
DC	2	6622235	2003-09-16	Keller et al			
DC	3	5555432	1996-09-10	Hinton et al			
DC	4	5655096	1997-08-05	Branigin			
DC	5	5844059	1999-03-16	Favor et al			
DC	6	5941983	1999-08-24	Gupta et al			
DC	7	5987594	1999-11-16	Panwar et al			
DC	8	6308259	2001-10-23	Witt			
DC	9	6108769	2000-08-22	Chinnakonda et al			
DC	10	6065105	2000-05-16	Zaldi et al			
DC	11	5978900	1999-11-02	Liu et al			
DC	12	6076153	2000-06-13	Grochowski et al			
DC	13	6321328	2001-11-20	Karp et al			
DC	14	5778219	1998-07-07	Amerson et al			

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DC	15	5794029	1998-08-11	Babaian et al
DC	16	5915117	1999-06-22	Ross et al
DC	17	6336178	2002-01-01	Favor
DC	18	5751983	1998-05-12	Abramson et al
DC	19	5781790	1998-07-14	Abramson et al
DC	20	5751985	1998-05-12	Shen et al
DC	21	6098166	2000-08-01	Leibholz et al
DC	22	5724536	1998-03-03	Abramson et al
DC	23	6070238	2000-05-30	Feiste et al
DC	24	5802575	1998-09-01	Greenley et al

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	21-DEC.-2005

<b>Form PTO-1449</b> (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		ATTY. DKT. NO. 5500-89400 APPLICANT: Filippo, et al.	SERIAL NO. 10/615,101 GROUP: 2121
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**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
DC	A1	96/12227	4/25/96	WO			
DC	A2	00/11548	3/2/00	WO			
DC	A3	709 770	5/1/96	EP			
DC	A4	97/27538	7/31/97	WO			
DC	A5	2 281 422	3/1/95	GB			

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

DC	A6	Moshovos, et al., "Dynamic Speculation and Synchronization of Data Dependences," ACM, Published June 2, 1997, XP-000738156, pp. 181-193.
DC	A7	Chrysos, et al., "Memory Dependence Prediction Using Store Sets," IEEE, Published June 27, 1998, XP-000849912, pp. 142-153.
DC	A8	Leibholz, et al., "The Alpha 21264: A 500 MHz Out-of-Order Execution Microprocessor," IEEE, published February 23, 1997, XP-000751757, pp. 28-36.
DC	A9	Popescu, et al., "The Metaflow Architecture," IEEE Micro, June 1991, pp. 10-13 and 63-73.
DC	A10	Gwennap, "Digital 21264 Sets New Standard," Microprocessor Forum, Vol. 10, NO. 14, Microdesign Resources, October 28, 1996, pp. 103-108.
DC	A11	Mike Johnson, "Superscalar Microprocessor Design," Advanced Micro Devices, Prentice Hall 1991, pp. 130-132, 147-163.
DC	A12	Moshovos, et al., "Streamlining Inter-operation Memory Communication via Data Dependence Prediction," IEEE Proceedings of Micro-30, December 1997, 11 pages.
DC	A13	Moshovos, et al., "Speculative Memory Cloaking and Bypassing," October 1999, 15 pages.
DC	A14	Gopal, et al., "Speculative Versioning Cache," February 1998, 11 pages.

EXAMINER:

*D. H. C.*DATE CONSIDERED: 21-DEC-2005

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.



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init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
X	1	6694424	2004-02-17	Keller et al			
X	2	6266744	2001-07-24	Hughes et al			
X	3	6393536	2002-05-21	Hughes et al			
X	4	6108770	2000-08-22	Chrysos et al			
X	5	6502185	2002-12-31	Keller et al			
X	6	6542984	2003-04-01	Keller et al			
X	7	5850533	1998-12-15	Panwar			
X	8	6651161	2003-11-18	Keller et al			

Signature

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	21-DEC-2005